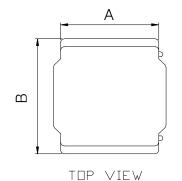
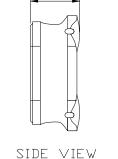
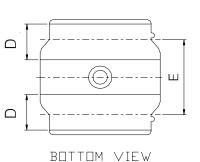
<u>JANTEK</u>

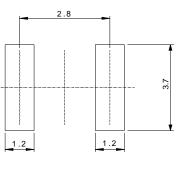
SMD Power Coil-JNR 4018-Series-M





С





Recommended Land-Pattern

(UNIT: mm)

Dimensions: (mm)

Part No.	Α	В	С	D	Е	
JNR 4018	4.0 ± 0.2	4.0 ± 0.2	1.8 Max.	1.1 ± 0.2	2.5 ± 0.2	

Series List

No.	Part No.	L	SRF Min.	RDC ±20%	Isat Max.	Irms Max.
		(μH)	(MHz)	(Ω)	(mA)	(mA)
1	JNR 4018-470M-M	47.0	10	0.65	570	420
2	JNR 4018-680M-M	68.0	8.3	1.00	470	320
3	JNR 4018-101M-M	100.0	6.5	1.50	400	270
4	JNR 4018-151M-M	150.0	5.5	2.50	310	220
5	JNR 4018-221M-M	220.0	4.0	4.00	270	170

1.Test Frequency : 100KHz

2.Tolerance : M ± 20%

3.Isat : The value of current causes a 30% inductance reduction from initial value.

4.Irms : The value of current causes a 40 $^\circ\!\mathrm{C}$ temperature rise.

5.Rated Current: Either Isat or Irms whichever is smaller.

6.Operating Temperature Range : -25° C to $+120^{\circ}$ C (Including self-temperature rise)

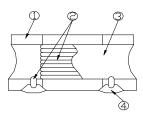
7.Storage Temp. Range : -40 $^\circ\!\mathrm{C}$ to +85 $^\circ\!\mathrm{C}$



 Type
 JNR 4018

 Q'TY/Reel
 3500

Structural Drawing

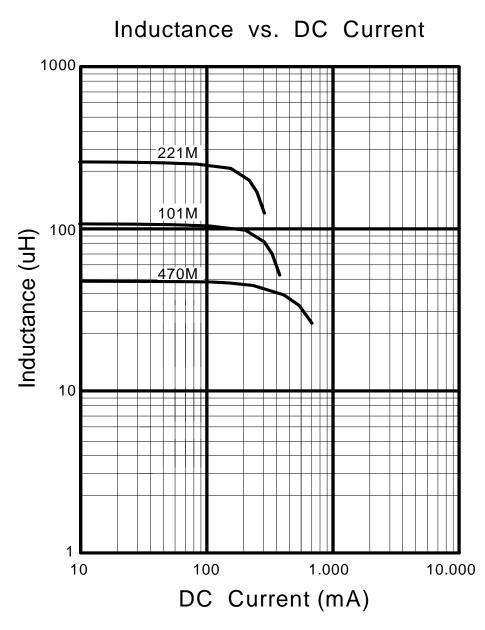


(Magnetic Shielded Type)

- ① Ferrite core.
- ② Winding wire
- ③ Over-coating resin.
- ④ Electrode

Ni-Zn ferritePolyurethane-copper wireEpoxy resin, containing ferrite powderExternal electrode (substrate)AgExternal electrode (base plating)Ni-SnExternal electrode (top surface solder coating)Sn-Ag-Cu

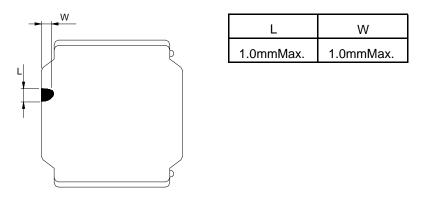
Electrical Curve



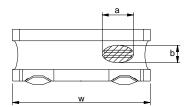


Core Chipping

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension



Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



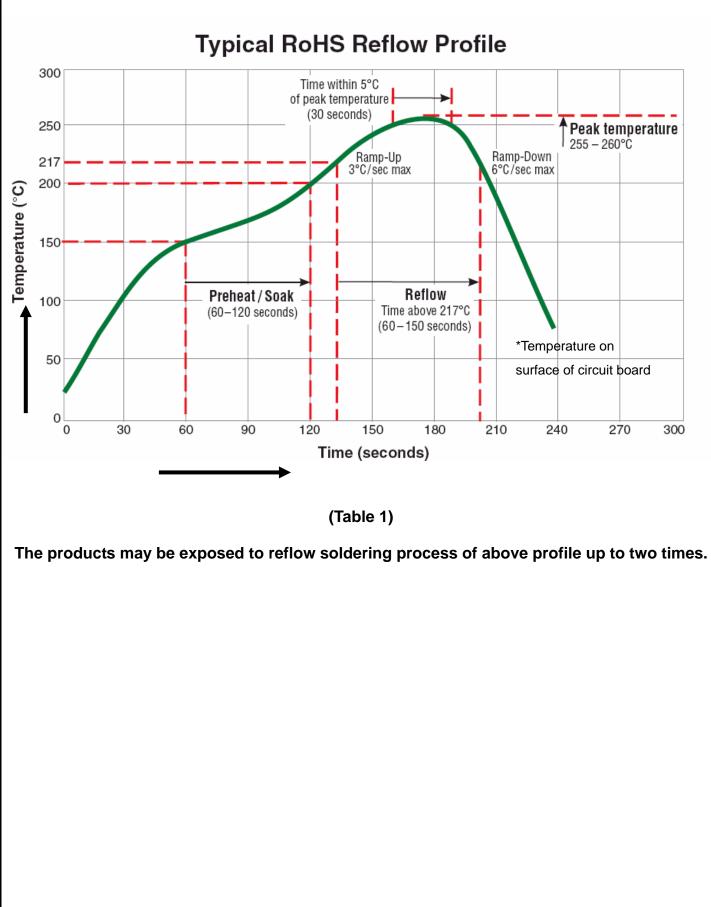
0 Width direction (dimension a): Acceptable when a<=w/2 Nonconforming when a>w/2

② Length direction (dimension b): Dimension b is not specified.
 ③ When total area of exposed wire occurring to each sides is

not greater than 50% of coating resin area, that is acceptable.



1. Reflow Profile Chart (Reference):





		e /Environmental Test Perf	
	Test Item	Standard	Test method
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
			20 10 Force R230 Rod
TICS			R5 45 ± 2 45 ± 2 45 ± 2 0.8 1.4 0.8
ERIS			Land dimensions
HARACTI			Test board size :100×40×10 Test board material I: glass epoxy-resin Solder cream thickness:0.1 Unit: mn
MECHANICAL CHARACTERISTICS	Adhesion of Terminal Electrode	Shall not come off PC board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.
IECHAN		→ 10 N, 5 s	
2			Applied force:10 N to X and Y directions Duration:5 s. Solder cream thickness:0.1 mm (Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s
			R0.5mm



Test Item	Standard		Test method						
Resistance to	\triangle L/L:within±10%	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1.Then							
Vibration	No abnormality			nitted to below test conditions					
	observed			Frequency range 10Hz~55Hz					
	In appearance	Total	Total Amplitude 1.5mm(May not exceed acce 196 m/S ²)						
		Sweep	oing Method	10Hz to 55	5Hz to 10 Hz	for 1 min.			
			Y, and Z axis.						
Resistance to	\triangle L/L:within±10%				sed to reflow				
Soldering heat	No abnormality	230±5 deg C for 40 seconds, with peak temperature 260±5 deg C for 5 seconds, 2 times.							
(Reflow)	observed	Test ho	ard thicknes	s·1 0 mm					
	In appearance		ard material	ky-resin					
Solder ability	At least 90% of surface	The test samples shall be dipped in flux, and then							
	of terminal electrode is	Immersed in molten solder as shown in below table. Flux: Methanol solution containing rosin 25%							
	covered by new solder.		^r Temperatur		5±deg C	7			
			· · · · · · · · · · · · · · · · · · ·		±1.0 S.				
		Imme	ersing Speed	l 2	5 mm/s				
Temperature Characteristics	 △L/L:within±20% No abnormality observed In appearance 	Range With re	within -25 de	eg C to +85 ductance v	5 deg C.	at temperature deg C, change			
Thermal shock	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to By the reflow soldering conditions sho The test samples shall be placed at sp Shown in below table in sequence. The temperature cycle shall be repeat			ons shown in ed at specifie ence.	Table 1. ed			
		Conditions of steps for 1 cycle							
		Step Temperature			Time(m	nin)			
		1	-40±3 c	leg C	30±3	3			
		2	Room		3 maxin	num			
		3	85±2 deg C		30±3				
		4	Room	lemp	3 maxim	num			
Low Temperature life Test	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to the test board b The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test Conditions as shown in below table.				able 1.			
		Ten	nperature		2 deg C				
		Time		500 +	-24/-0 h				

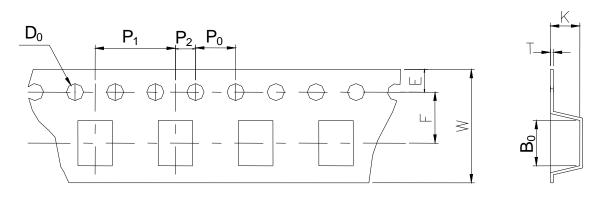


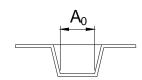
	Test Item	Standard	Test method
	Loading at high temperature life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously as shown in below table.
			Temperature 85±2 deg C
			Applied current (Refer to Page 2)
			Time 500+24/-0 h
ENVIRONMENT TESTS	Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table.
L N E			Temperature 60±2 deg C Humidity 90~95%RH
NNC			Humidity 90~95%RH Time 500+24/-0 h
ENVIR	Loading under Damp heat life test	△L/L:within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table.
			Temperature 60±2 deg C
			Humidity 90~95%RH
			Applied current (Refer to Page 2))
			Time 500+24/-0 h



- 3. Tape & Reel Packaging Dimensions:
 - 3-1 Dimensions

Unit: mm





Ao	Bo	W	F	E	P 1	P ₂	P ₀	D ₀	Т	K
4.3 ±0.1	4.3 ±0.1	12.0 ±0.3	5.5 ±0.1	1.75 ±0.1	8.0 ±0.1	2.0 ±0.1	4.0 ±0.1	Ф1.5 +0.1 -0	0.3 ±0.05	2.1 ±0.1

3-2 Direction of rolling

