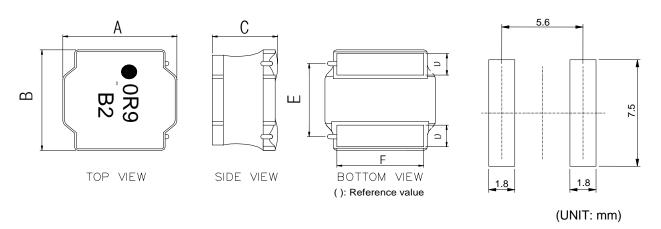


Recommended Land-



■ Dimensions: (mm)

Part No.	Α	В	С	D	E	F
JNR 8040-R90N~6R8N	8.0 ± 0.2	8.0 ± 0.2	4.2 Max.	1.6 ± 0.3	5.6 ± 0.3	5.5 ± 0.3
JNR 8040-100M~220M	8.0 ± 0.2	8.0 ± 0.2	4.0 Max.	1.6 ± 0.3	5.6 ± 0.3	5.5 ± 0.3

Series List

		L	SRF	RDC	Isat	Irms
No.	Part No.		Min.	±20%	Max.	Max.
		(μH)	(MHz)	(Ω)	(mA)	(mA)
1	JNR 8040-R90N-MS	0.9	85	0.006	13000	7800
2	JNR 8040-1R4N-MS	1.4	63	0.007	10000	7000
3	JNR 8040-2R0N-MS	2.0	50	0.009	8100	6300
4	JNR 8040-3R6N-MS	3.6	34	0.015	6400	4900
5	JNR 8040-4R7N-MS	4.7	30	0.018	5400	4100
6	JNR 8040-6R8N-MS	6.8	24	0.025	4400	3700
7	JNR 8040-100M-MS	10	22	0.034	3800	3100
8	JNR 8040-150M-MS	15	16	0.050	2900	2400
9	JNR 8040-220M-MS	22	13	0.066	2400	2200

1.Test Frequency: 100KHz

2.Tolerance : $N \pm 30\%$; $M \pm 20\%$

3.Isat: The value of current causes a 30% inductance reduction from initial value.

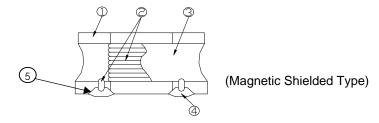
4.Irms : The value of current causes a 40℃ temperature rise.

PACKAGE

Type	JNR 8040
Q'TY/Reel	1000



■ Structural Drawing



1. Ferrite core. Ni-Zn ferrite

2. Winding wire Polyurethane-copper wire

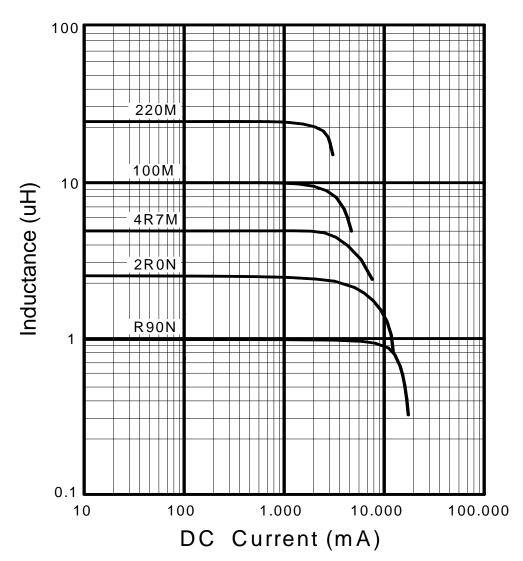
3. Over-coating resin. Epoxy resin, containing ferrite powder

4. External electrode Sn-Ag-Cu

5. Base plating Phosphor Bronze (using the Epoxy adhesive)

Electrical Curve

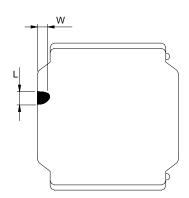
Inductance vs. DC Current





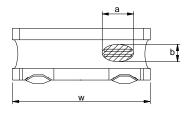
■ Core Chipping

The appearance standard of the chipping size in top side, of bottom side ferrite Core is following dimension

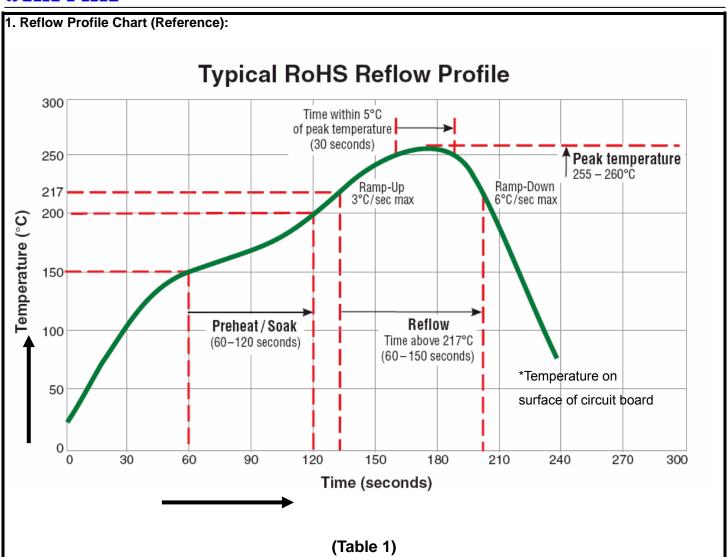


L	W			
2.0mmMax.	2.0mmMax.			

Exposed wire tolerance limit of coating resin part on product side Size of exposed wire occurring to coating resin is specified below.



- ① Width direction (dimension a): Acceptable when a<=w/2
 Nonconforming when a>w/2
- ② Length direction (dimension b): Dimension b is not specified.
- ③ When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.



The products may be exposed to reflow soldering process of above profile up to two times.



	Test Item	Standard	Test method
	Resistance to Deflection	No damage.	The test samples shall be soldered to the test board by the reflow soldering conditions show in Table 1. As illustrated below, apply force in the direction of the Arrow indicating until deflection of the test board Reaches to 2 mm.
			Force Rod F230 5.1
STICS			R5 Board Sample 45±2 45±2 1.5 1.5
ERIO ERIO			Land dimensions
CI			Test board size: 100×40×10
Ą			Test board material I: glass epoxy-resin Solder cream thickness:0.1 Unit: mm
SAL CHA	Adhesion of Terminal Electrode	Shall not come off PC board	The test samples shall be soldered to the test board By the reflow soldering conditions shown in Table 1.
MECHANICAL CHARACTERISTICS			■ 10 N, 5 s
M			Applied force:10 N to X and Y directions
			Duration:5 s. Solder cream thickness:0.1 mm
			(Refer to recommended Land Pattern Dimensions Defined in "Precaution")
	Body strength	No damage	Applied force :20 N Duration :10 s
			R0.5mm ———————————————————————————————————
			0.6W



Test Item	Standard	Test method			
Resistance to Vibration Resistance to Soldering heat	Standard △L/L:within±10% No abnormality observed In appearance △L/L:within±10% No abnormality observed	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. Then It shall be submitted to below test conditions Frequency range 10Hz~55Hz 1.5mm(May not exceed acceleration 196 m/S²) Sweeping Method 10Hz to 55Hz to 10 Hz for 1 min.			
(Reflow)	In appearance	Test board thickness:1.0 mm Test board material :glass epoxy-resin			
Solder ability	At least 90% of surface of terminal electrode is covered by new solder.	The test samples shall be dipped in flux, and then Immersed in molten solder as shown in below table. Flux: Methanol solution containing rosin 25% Solder Temperature 245±deg C Time 5±1.0 S. Immersing Speed 25 mm/s			
Temperature Characteristics	△L/L:within±20% No abnormality observed In appearance	Measurement of inductance shall be taken at temperature Range within -25 deg C to +85 deg C. With reference to inductance value at +20 deg C, change Rate shall be calculated.			
Thermal shock	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to test board By the reflow soldering conditions shown in Table 1. The test samples shall be placed at specified Shown in below table in sequence. The temperature cycle shall be repeated 100 cycles. Conditions of steps for 1 cycle			
		Step Temperature Time(min) 1 -40±3 deg C 30±3 2 Room Temp 3 maximum 3 85±2 deg C 30±3 4 Room Temp 3 maximum			
Low Temperature life Test	△L/L:within±10% No abnormality observed In appearance	The test samples shall be soldered to the test board by The reflow soldering conditions shown in Table 1. After that, the test samples shall be placed at test Conditions as shown in below table. Temperature -40±2 deg C Time 500 +24/-0 h			



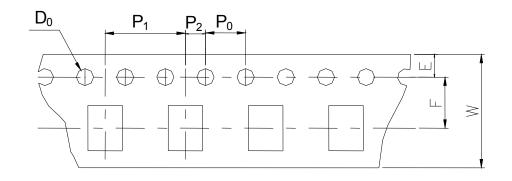
The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuousl as shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuousl as shown in below table. Temperature 85±2 deg C Applied current (Refer to Page 2) Time 500+24/-0 h The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table. Temperature 60±2 deg C Humidity 90-95%RH Time 500+24/-0 h The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table. Temperature 60±2 deg C Humidity 90-95%RH The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table. Temperature 60±2 deg C Humidity 90-95%RH Rated current Rate								
The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuousl as shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuousl as shown in below table. Temperature 85±2 deg C Applied current (Refer to Page 2) Time 500+24/-0 h The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table. Temperature 60±2 deg C Humidity 90-95%RH Time 500+24/-0 h The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table. Temperature 60±2 deg C Humidity 90-95%RH The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table. Temperature 60±2 deg C Humidity 90-95%RH Rated current Rate		Test Item	Standard	Test method				
Damp heat life test Damp heat life test Damp heat life test		temperature life	No abnormality observed in	The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and applied the rated current continuously				
SECOND Served Soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH Time 500+24/-0 h Loading under Damp heat life test No abnormality observed in appearance. The test samples shall be soldered to the test board by the reflor soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH Applied current Rated cur				Applied current (Refer to Page 2)				
Loading under Damp heat life test No abnormality observed in appearance. The test samples shall be soldered to the test board by the reflow soldering conditions shown in Table 1. The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH Applied current Rated current	NVIRONMENT TESTS	-	No abnormality observed	The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH				
Time (Refer to Page 2)) Time 500+24/-0 h	EN	Damp heat life	No abnormality observed	The test samples shall be placed in thermostatic oven set at specified temperature and humidity and applied the rated current continuously as shown in below table. Temperature 60±2 deg C Humidity 90~95%RH Applied current Rated current (Refer to Page 2))				

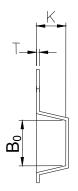


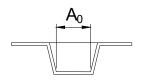
3. Tape & Reel Packaging Dimensions:





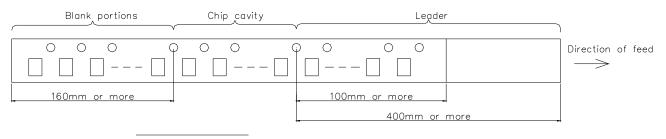






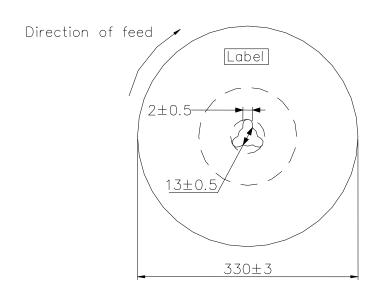
A 0	B ₀	W	F	Е	P ₁	P_2	P ₀	D ₀	T	K
8.30 ±0.1	8.30 ±0.1	16.0 ±0.3	7.5 ±0.1	1.75 ±0.1	12.0 ±0.1	2.0 ±0.1	4.0 ±0.1	Ф1.5 +0.1 -0	0.50 ±0.1	4.50 ±0.1

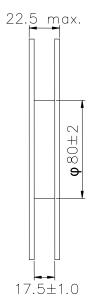
3-2 Direction of rolling



0000000 Direction of production Position of wire insertion

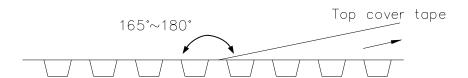
3-3 Reel





Label position:on the opposite sie of sprocket holes side of reel

3-4 Top tape strength

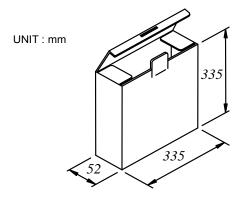


Peel-off strength: 0.1N~1.3N

Peel-off angle:165°~180°

Peel-off speed: 300mm/mm

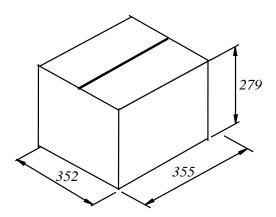
3-5 Dimensions of packing box (for Tape & Reel package)



CONSTURCTION:

THE CASE CONTAINS 2-16mm WIDE CARRIER TAPES.

Q'TY: 1,000/ REEL



TOTAL Q'TY: 8,000 PCS